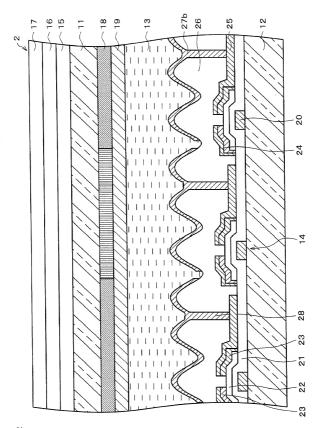
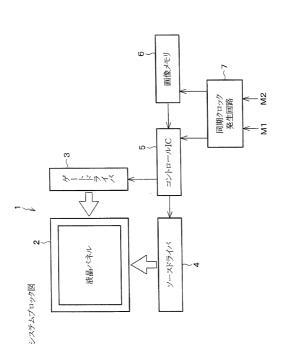
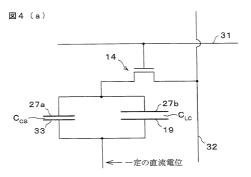


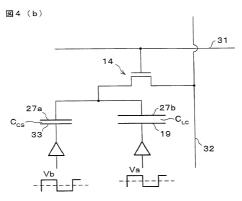
2/58





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5/58



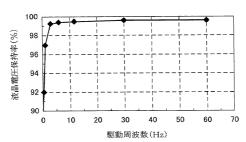
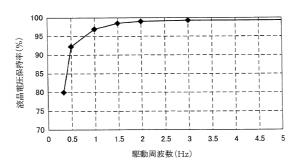
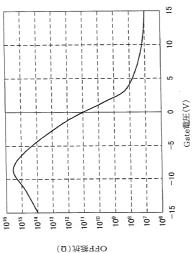
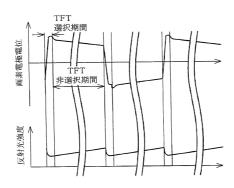


図5 (b)





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8/58



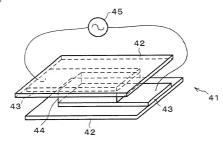
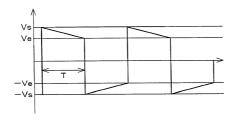
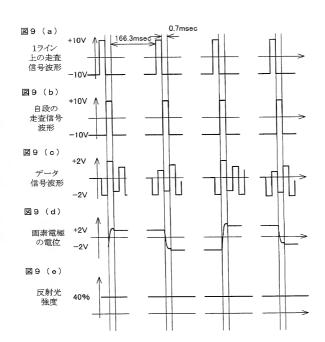
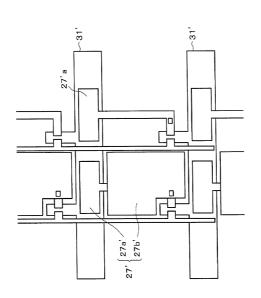


図8(b)

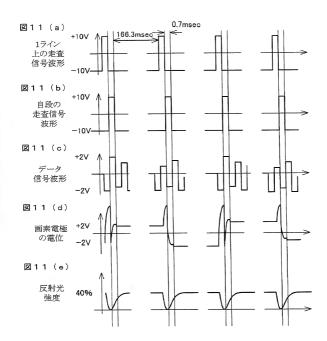


9/58

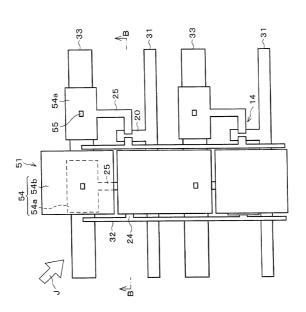




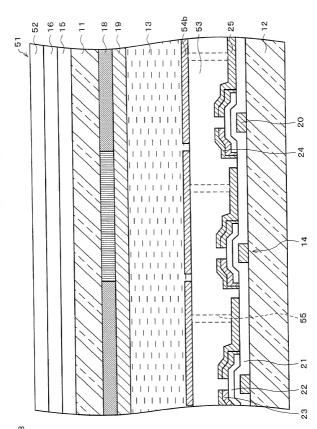
11/58



12/58



13/58



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14/58

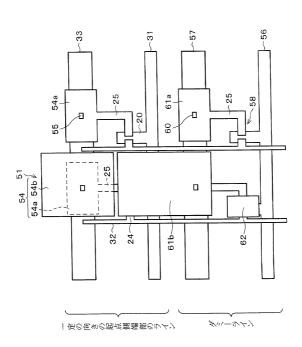
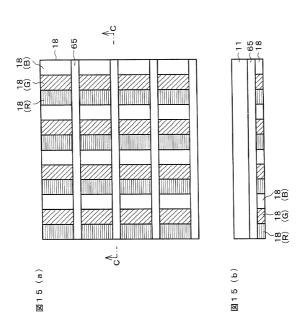


図14



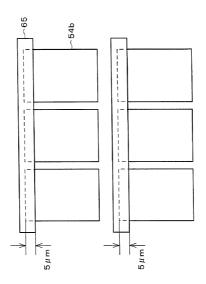
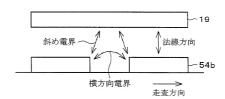


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図17



18/58

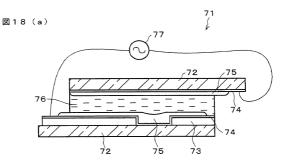
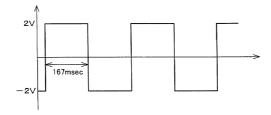
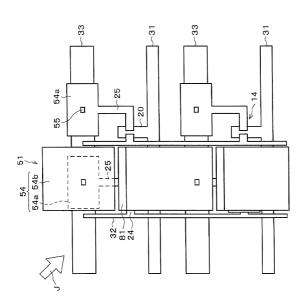
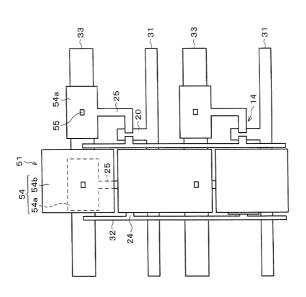


図18(b)



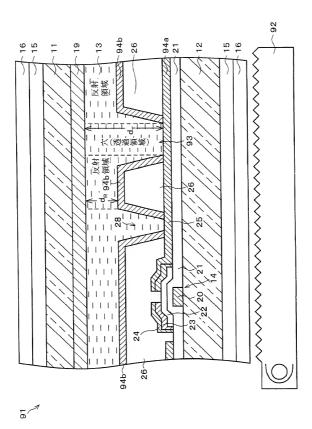


20/58



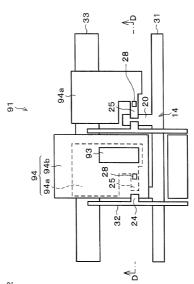


21/58



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22/58



⊠222

図23

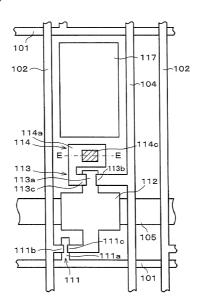


図24

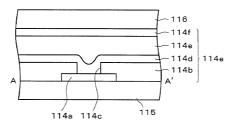
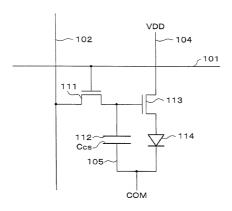
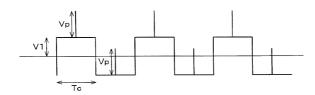


図25

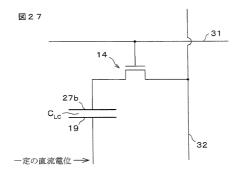


26/58

図26

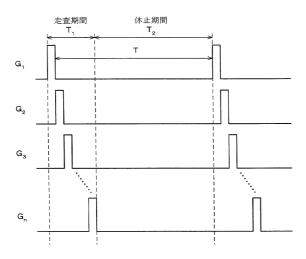


27/58



28/58

図28



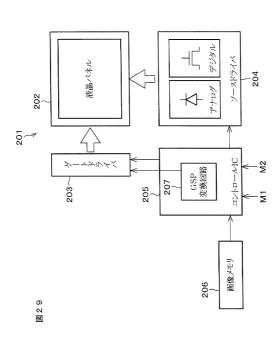
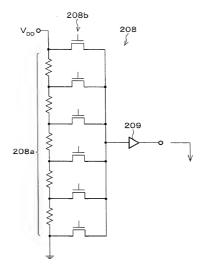
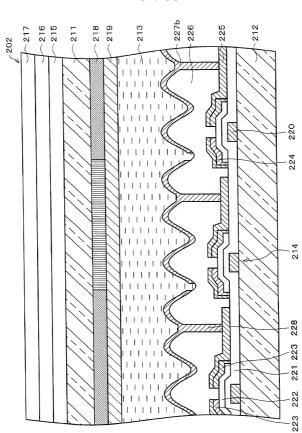


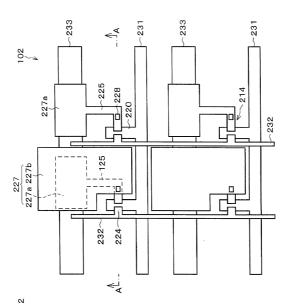
図30



31/58

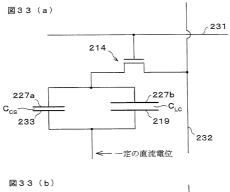


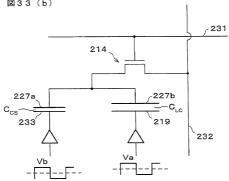
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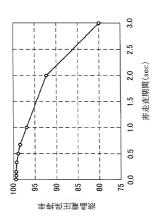
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33/58



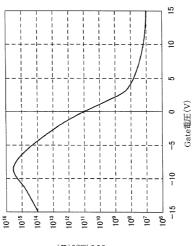


34/58



⊠34

35/58

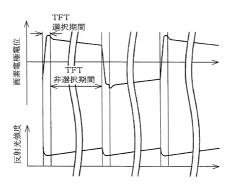


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36/58

図36



37/58

図37 (a)

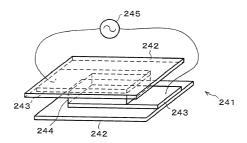
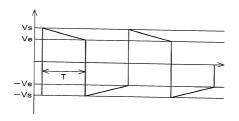
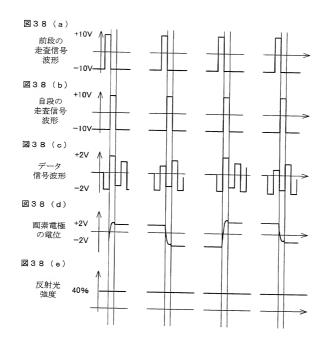


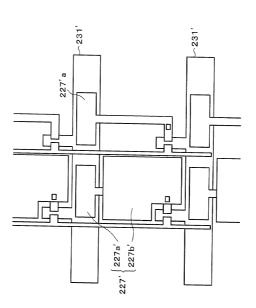
図37(b)



38/58

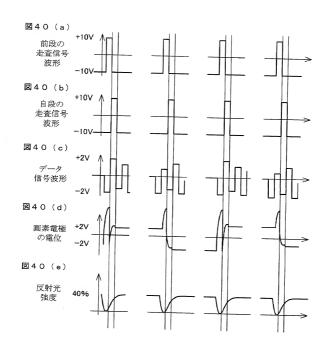


39/58

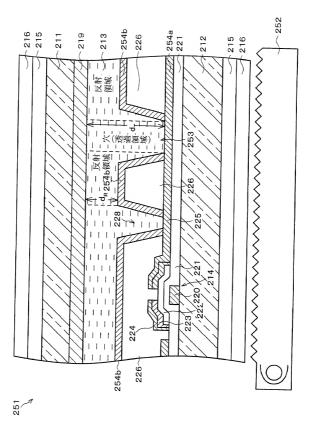


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40/58

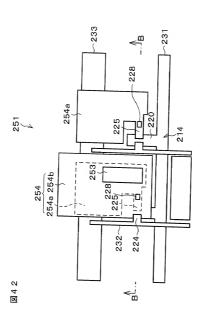


41/58



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42/58



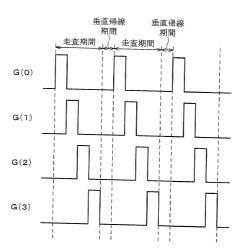
43/58

図43

		ソースドライバ			
		S(0)	S(1)	S(2)	S(3)
ゲートドライバ	G(0)	(0, 0)	(1, 0)	(2, 0)	(3, 0)
	G(1)	(0, 1)	(1, 1)	(2, 1)	(3, 1)
	G(2)	(0, 2)	(1, 2)	(2, 2)	(3, 2)
	G(3)	(0, 3)	(1, 3)	(2, 3)	(3, 3)

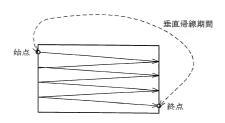
44/58

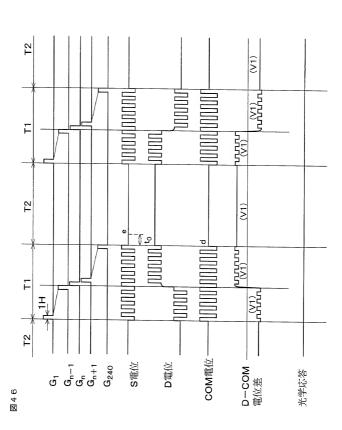
図44



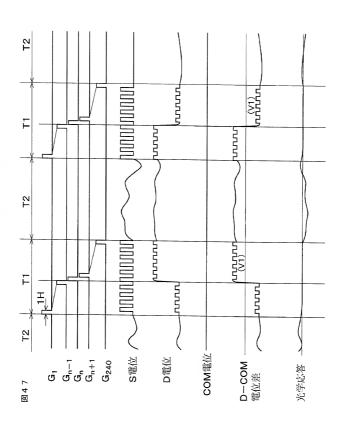
45/58

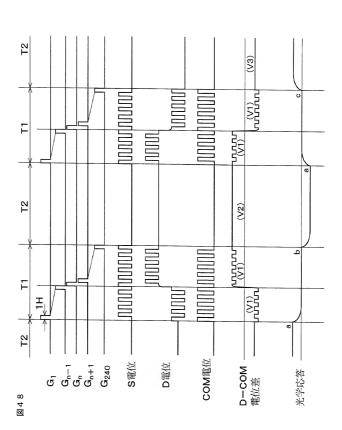
図45

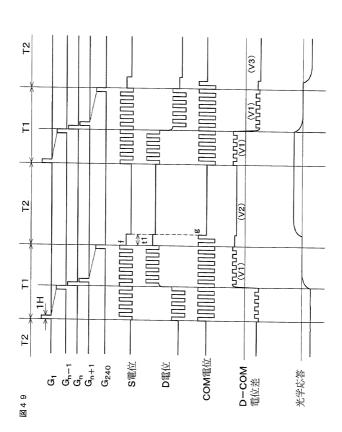


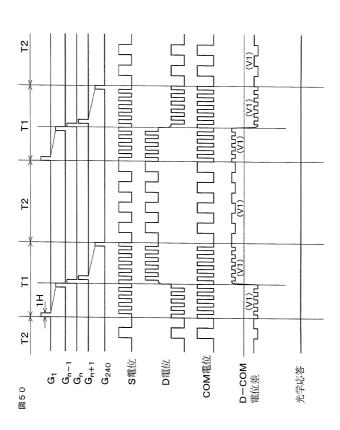


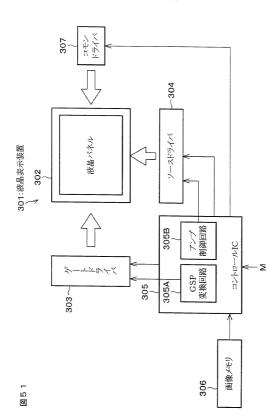


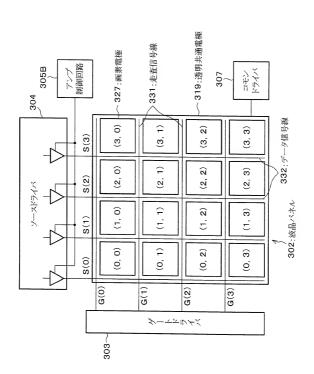




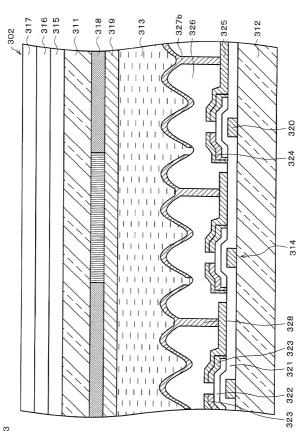








53/58



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